



5G Technology and Transceiver Architecture

A whitepaper by Omni Design Technologies

INTRODUCTION

5G is the dominant next-generation telecommunication network expected to bring transformational changes and benefits beyond legacy standards. As data demand continues to grow, and the applications of various network systems expand year after year, 5G will power the capacity growth needed by industries and consumers. It will support diverse use cases based on significantly improved data rates, latency, connectivity, and reliability. Deployment gained momentum in 2020, with both

field trials and commercial implementations on-going. This whitepaper provides an overview of the key technologies, challenges, and architectural solutions related to the 5G revolution.

APPLICATION AND USE OF 5G

5G deployment use cases can be categorized into three broad areas: enhanced mobile broadband (eMBB), ultra-reliable and low-latency communications (URLLC), and massive machine-type communications (mMTC) [1]. Each use case has a

different set of technical requirements with performance parameters including peak data rate, spectral efficiency, latency, connection density, reliability, and many more.

5G deployment will initially focus on eMBB that will offer a seamless experience to consumers. These applications include 4k and 8k streaming, AR/VR, and low-latency access to the cloud. Bandwidth may vary from 100MHz to 1GHz depending on the frequency band; lower frequency bands will have lower bandwidth due to the legacy narrow channel width in earlier communication standards. URLLC demands stringent requirement of 1ms latency and 99.999% reliability. It will be essential for vehicle-to-vehicle communication, mission critical applications, tactile applications, and certain industrial automations. Lastly, mMTC will have a large impact on IoT adoption. Wearables and sensors fall into this category, with potential connection densities of one million devices per square kilometer, a significant increase from 4G capabilities.

KEY ENABLING TECHNOLOGIES

The scope of technological advances in 5G is very broad and continues to grow. This section outlines a few of the most significant changes relative to what has been available in legacy standards, including millimeter-wave (mmWave), massive multiple input multiple output (MIMO) systems, and small cell.

Millimeter-Wave

5G New Radio (NR) is the global standard for the 5G network. As shown in Figure 1, it includes two distinct frequency bands: sub-6GHz (e.g., 2.4GHz and 5GHz), and mmWave that extends from 24GHz to 100GHz. In the sub-6GHz bands, existing infrastructure can be upgraded to enable rapid 5G deployment. 5G installations are currently in progress in the sub-6GHz domain. However, sub-6GHz bands are congested with numerous applications, including 4G, and have limited bandwidth constrained by the standards of previous generations. The mmWave spectrum is largely unused and offers significantly higher bandwidths -- up to 1GHz [1][2]. Several frequency

bands between 24GHz and 100GHz are being considered; 24GHz and 39GHz are of especially high interest [3].

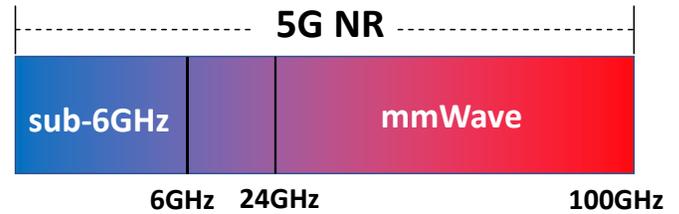


Figure 1: Available spectrum in 5G NR

One drawback of mmWave is its limited physical range compared to that of the sub-6GHz band. At high frequencies, propagation loss is severe, and signal is much more susceptible to blockage from obstacles. It is well known that rain attenuation, atmospheric and molecular absorption is exacerbated within the millimeter wave spectrum [1].

Massive MIMO

MIMO is a radio link technology that leverages multiple transmitters and receivers to improve signal performance. It has widely been used to enhance connectivity and its core techniques include spatial diversity, spatial multiplexing, and beamforming [4]. Spatial diversity provides robust and reliable communication via multipath signaling using multiple antennas. In this scheme, each antenna is spaced apart and sees a different input signal. The transmitted signal is mathematically estimated from collection of received signals. Spatial multiplexing increases data rate by utilizing multiple independent spatial streams between the transmitter and receiver antenna. A common configuration is 2x2 or 4x4 MIMO, in which data rate is improved theoretically by factor of 2 and 4, respectively. In practice, the actual throughput will depend on the signal-to-noise ratio (SNR) of the channel and correlation between the channels. Lastly, beamforming is a technique that directs signal to a specific direction and maximizes the power at the receiver while reducing interference for nearby receivers. This improves throughput and overall system capacity. Figure 2 shows an illustration of massive MIMO beamforming.

In 5G networks, massive MIMO will deploy a much higher number of antennas and transceivers relative to legacy systems, further extending the performance gains of MIMO. Theoretically, MIMO can benefit from an arbitrarily large number of antennas. Current commercial solutions include 64TX and 64RX for base station deployment compared to the 4 transceivers in 4G LTE [5]. A massive MIMO radio will contain a top panel for antenna elements, and additional layers for analog/RF and digital circuitry. These components are highly integrated in a few packed boards to minimize footprint and interconnection.

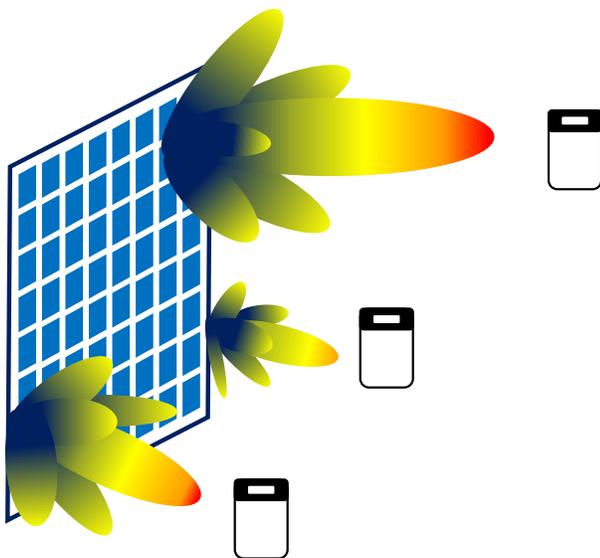


Figure 2: Illustration of massive MIMO beamforming

Small Cell

Small cells are used to cover smaller geographical areas than macrocells, and consist of femtocells, picocells, and microcells. These cell types have varying coverage radius, output power, and intended number of users. Small cells are needed in ultra-dense networks to increase data capacity, and to cover small spaces both indoors and outdoors. They can service inter-site distance of 200m and improve both coverage and capacity [6]. It is critical in mmWave because signals at these frequencies cannot penetrate buildings and have limited range. Sub-6GHz base stations will leverage existing networking infrastructures, and will provide larger areas of coverage, as shown in Figure 3. In contrast, mmWave

will rely on new small cell infrastructures to serve smaller hotspots where data demand is unusually high [7].

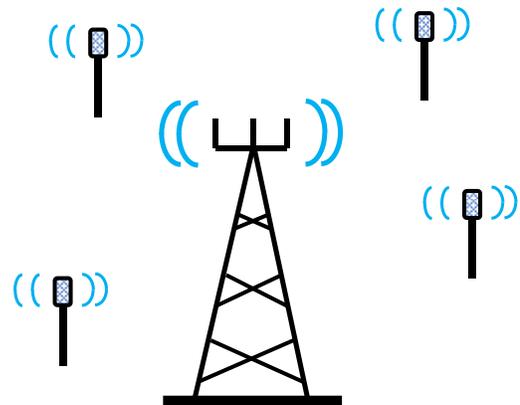


Figure 3: Small cells enabling expanded coverage area

HARDWARE ARCHITECTURES

The complexity of 5G hardware implementation is nontrivial. Large antenna arrays lead to many signal channels and increased need for processing power. Different beamforming methods and transceiver architectures must be explored to determine feasible solutions based on individual applications [7].

Beamforming Solutions

Massive MIMO uses large antenna arrays and requires many RF chains consisting of data converters, filters, mixers, switches, PGAs, LNAs, etc. The need for large numbers of RF chains leads to increased power consumption and cost. Therefore, beamforming approaches should be carefully considered as it will determine the number of components and configurations in the RF chain[1][8].

Beamforming can be done in digital, analog, or hybrid mode as shown in Figure 4. Digital beamforming has the most flexible signal processing capabilities since the amplitude and phase control is done in digital baseband. However, it requires an RF chain for each antenna and results in a large number of components, high power consumption, and increased complexity. In systems with very large number of antennas, as would be plausible in mmWave deployment, the correspondingly large number of RF transceivers

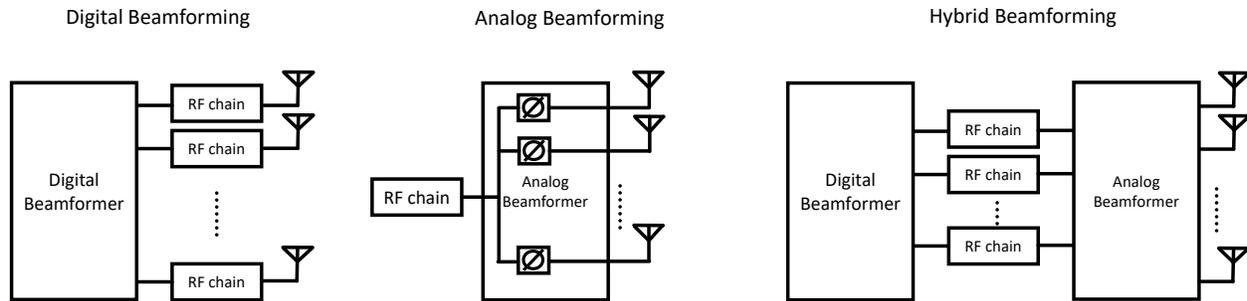


Figure 4: Digital, analog and hybrid beamforming architectures

needed for implementing digital beamforming would significantly increase implementation cost.

Analog beamforming is the traditional approach and requires only one RF chain. Signals are adjusted in the analog domain via phase shifters and gain control before the antenna. Although simple and cost effective, analog beamforming offers less reconfigurability and spatial resolution.

Hybrid beamforming is of high interest as it combines both digital and analog beamforming. It offers greater flexibility than analog beamforming, reduced complexity, and lower cost than pure digital beamforming owing to the lower number of RF chains required. Signal processing is carried out in both the digital and analog domains. Hybrid beamforming reduces complexity and hardware cost relative to digital beamforming, while still being able to facilitate multi-stream processing in contrast to analog beamforming.

Transceiver Silicon Implementations

The implementation of the transceiver system in discrete solutions leads to a large footprint and high power consumption. It would be difficult to integrate many channels on a PCB. The components are bulky and it would be challenging to have many high speed interfaces to move data between the digital processing module and the data converters. This approach does not scale efficiently in 5G as the

number of channels are drastically higher than in previous generations, and a more flexible platform is needed to handle the evolving requirements of 5G. The solution ultimately requires the integration of analog modules into the SoC. This significantly reduces footprint and enables massive MIMO to be natively supported. Also, it eliminates complexity of routing high-speed signals between discrete components.

Error! Reference source not found. shows a direct-RF transceiver suitable for sub-6GHz 5G **Error! Reference source not found..** Unlike a superheterodyne transceiver, the signal up/down conversions are handled digitally in the direct-RF architecture via a numerically controlled oscillator (NCO). This architecture improves platform flexibility and performance while eliminating analog and RF impairment. The programmability makes the platform suitable for various sub-6GHz frequencies since all frequency translations are handled digitally.

In the transmitter, the signal is digitally upconverted to RF. A higher up-sample ratio eases the filtering requirements that follows the DAC. The DAC output is an RF signal that goes through image filtering and amplification via VGA and PGA. In the receiver, the LNA, VGA, and filtering provides the necessary analog signal processing before the data conversion. An RF signal is sampled by the ADC and finally down converted in the digital domain.

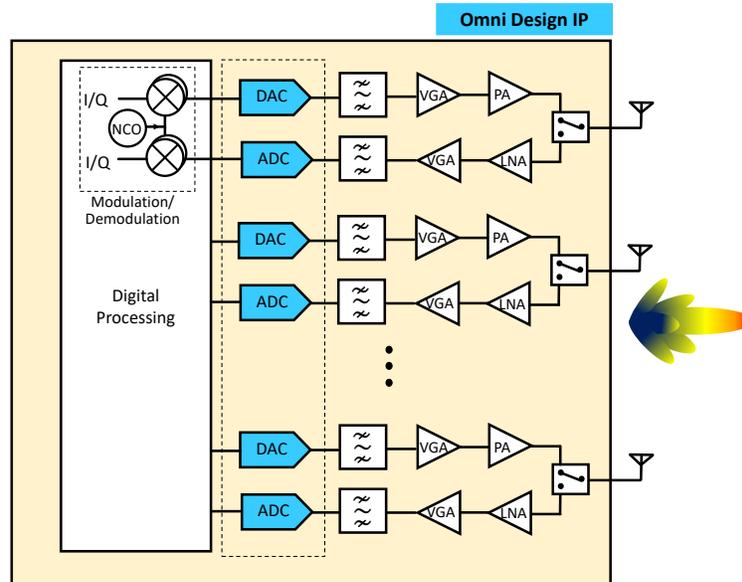


Figure 5: Direct-RF architecture

The intermediate-frequency (IF) architecture shown in **Error! Reference source not found.** is a potential solution for mmWave application. Since mmWave applications run on much higher frequency bands than in sub-6GHz, it is necessary to have signal processing at the transceiver front-end in analog/RF domain and handle signal processing between baseband and IF in the digital domain. The transmitter upsamples and performs upconversion to IF frequency in the digital domain similarly to in the direct RF transmitter. After filtering, the signal carrier

is translated to the mm-wave frequency in the phased-array IC. In the receiver, the signal is down-converted to IF frequency in the phased-array IC and goes through additional down conversion digitally. Although the architecture in Figure 6 is for digital beamforming where the phase is adjusted via digital signal processing, the IF architecture can be extended for other beamforming approaches as well. For example, the phase shift can be accomplished in the mmWave frequency [9].

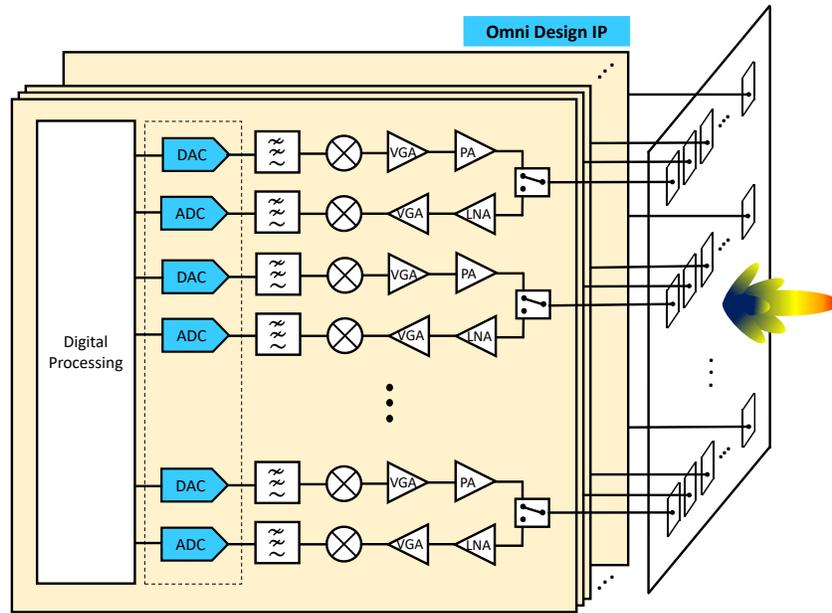


Figure 6: IF architecture

OMNI DESIGN’S SOLUTION FOR 5G

The speed requirements for 5G data converters range from hundreds of MSPS to multi-GSPS, and from 10 to 14-bits in resolution. Advanced nodes enable better power efficiency and a more compact area. Omni Design offers a wide array of data converter IP solutions for 5G applications. A partial list of data converter IP products is listed in Table 1. Solutions are offered in multiple process nodes from 28nm to advanced FinFET nodes, and Omni Design’s patented and proprietary technologies enable data converters to operate at higher sampling rates and precision while significantly reducing power consumption. These proprietary techniques also enable design of high-speed converters at low supply voltages that are typically associated with advanced finFET process nodes. Also, the ADCs and DACs include clock synchronization circuits to support phased array implementations.

Product Name	Resolution (bits)	Speed (MSPS)
ODT-ADP-14B1P2G-28	14	1200
ODT-ADP-14B600M-28	14	600
ODT-ADP-14B300M-28	14	300
ODT-ADS-12B6G-16	12	6000
ODT-ADS-12B2G-16	12	2000
ODT-DAC-12B7G-16	12	7000

Table 1. Partial list of data converter IPs available from Omni Design

The ADC IP products are designed to include any required foreground and background calibration for correcting various non-idealities such as time-interleaving artifacts and capacitor mismatch. Omni Design’s proprietary interleaving and calibration technology enables converters to be compatible with wireless communication requirements. This technology removes any unwanted spectral notches that can be observed in many other time interleaving correction algorithms. For example, ODT-ADS-12B6G-16, part of our Lepton™ (12-bit) ADC family, in Figure 7 is a high-performance time-interleaved ADC. This 12-bit 6GSPS ADC supports input signals up to the Nyquist frequency, and features SNR and SFDR of 56dB and 62dB, respectively. The designs of the data converters leverage Omni Design’s proprietary platform-based development methodology that enables the design of these converters using modular building blocks that results in a substantial reduction in schedule and product risk.

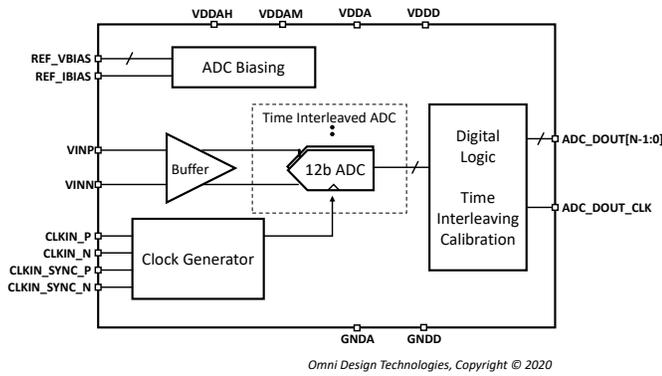


Figure 7: ODT-ADS-12B6G-16 - 12-bit 6GSPS ADC

Omni Design’s RF-DAC IPs can be leveraged to generate wide-bandwidth modulated waveforms. For example, ODT-DAC-12B7G-16, part of our Lepton™ (12-bit) DAC family, in Figure 8 is a high-performance current-steering 12-bit DAC that operates at an update rate of up to 7GSPS. The DAC uses a proprietary architecture that reduces harmonic and intermodulation distortions at high output frequencies and amplitudes. It also includes a direct digital synthesizer for signal generation.

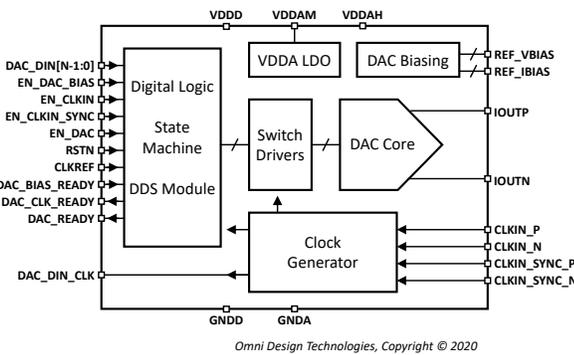


Figure 8. ODT-DAC-12B7G-16: 12-bit 7GSPS DAC

SUMMARY

The acceleration of 5G deployment will demand significant innovation across multiple domains to fulfill its potential. Among the key challenges is the implementation of flexible and cost-efficient hardware. Different architectures will need to be considered based on performance, capacity, cost, and

Integrating multiple high-speed data converters at the AFE level requires extensive design and verification efforts. Omni Design provides all the necessary deliverables and support to enable the integration of the AFE into the customer ASIC, including analog, digital, scan chain, behavioral models, and detailed integration documentation. Third-party phase-locked-loop (PLL) IP products are included in the AFE, and any clock division, synchronization, or routing is handled internally. The ODT-AFE-4T4R-16 in Figure 9 is an example of a high performance AFE. It supports four ADCs and four DACs. The AFE also includes bandgap references and low-dropout voltage regulators (LDOs) to generate clean supply voltages and references to multiple analog/mixed-signal IPs.

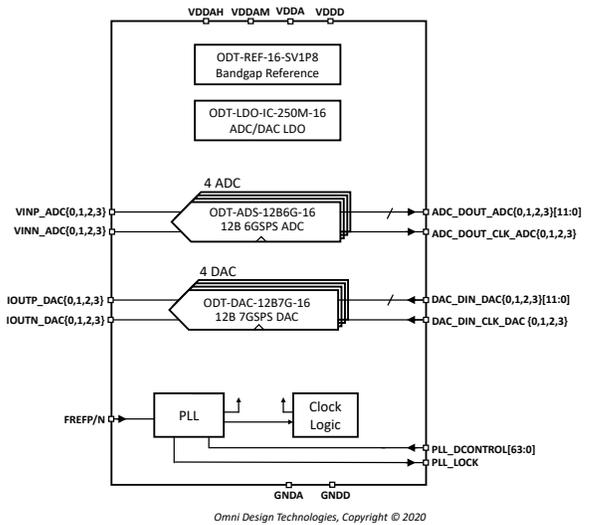


Figure 9: ODT-AFE-4T4R-16 - 5G AFE

footprint requirements. High-performance silicon IP will play an essential role in facilitating the overall system development. A portfolio of IP products with state-of-the-art power efficiency, form factors, and integration support are needed to meet the stringent and evolving specifications. Omni Design is already delivering the leading mixed-signal solutions necessary to enable the coming 5G era.

For a full listing of IP Cores that are required for 5G solutions, please visit our website at omnidesigntech.com or contact Omni Design at info@omnidesigntech.com.

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